

List of Patents and Publications for
Applicant's Information Disclosure Statement

APPLICANT: Holland, et al.

(Use several sheets if necessary)

FILING DATE:
Concurrently HerewithGROUP:
To be Assigned

Reference Designation

US PATENT DOCUMENTS

Examiner Initials	Document Number	Date	Name	Class/Subclass	Filing Date (If. Appro.)
J. J. ↓ J. J.	5,008,878	04/16/91	Ahmadi et al	370/60	10/14/88
	5,835,971	11/10/98	Ikeda	711/220	12/5/95
	5,598,568	01/28/97	Frisch	395/800	05/06/93
	5,517,662	05/14/96	Coleman et al	395/800	11/08/94
	5,751,934	05/12/98	Han et al	345/182.02	11/12/96
	5,890,007	03/30/99	Zinguuzi	395/800.06	02/26/96
	5,179,669	01/12/93	Peters	395/325	08/22/88
	5,179,715	01/12/93	Andoh et al	395/800	06/05/91
	5,619,650	04/08/97	Bach, et al	395/200.01	09/21/95
	5,774,698	06/30/98	Olnowich	395/500	03/03/97
	5,842,034	11/24/98	Bolstad, et al	395/800.11	12/20/96

FOREIGN PATENT DOCUMENTS

Document Number	Date	Country	Class/Subclass	Translation Yes No
JP 9062641	07 March 1997	JP	H04L 12/40	X

OTHER ART (Including Author, Title, Data, Pertinent Pages, etc.)

IBM Technical Disclosure Bulletin, V 35, No. 1B, 6/92 "Computer Memory Organization Using Fast Nonvolatile Storage" pp 380-382

IBM Technical Disclosure Bulletin, V. 33, No. 10A, 3/91 "Four-Address Virtual Memory" pp 234-237

IBM Technical Disclosure Bulletin, V. 33, No. 10A, 3/91 "Parallel Long Move

Instruction" pp 21-22

IBM Technical Disclosure Bulletin, V.32, No. 12, 3/90 "Process Control Executive - A
New Paradigm for Operating System Design" pp 414-419



Examiner

J. G. F.

Date Considered

8/5/03

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through
citation if not in conformance and not considered. Include copy of this form with next communication to applicant.